

disposed in an integrated circuit, said system comprising:

a first host processor system coupled to said multimedia processor;

a second local processor disposed within said multimedia processor for controlling the operation of said multimedia processor;

a data transfer switch disposed within said multimedia processor and coupled to said second processor for transferring data to various modules of said multimedia processor;

a data streamer coupled to said data transfer switch, and configured to schedule simultaneous data transfers among a plurality of modules disposed within said multimedia processor, at least one of which is a cache memory, in accordance with corresponding channel allocations;

an interface unit coupled to said data streamer having a plurality of input/output (I/O) device driver units;

a multiplexer coupled to said interface unit for providing access between a selected number of said I/O device driver units to external I/O devices via output pins; and

a plurality of external I/O devices coupled to said multimedia processor.

28. (Amended) An integrated multimedia system having a multimedia processor disposed in an integrated circuit, said system comprising:

a processor disposed within said multimedia processor for controlling the operation of said multimedia processor;

a data transfer switch disposed within said multimedia processor and coupled to said processor for transferring data to various modules of said multimedia processor;

a data streamer coupled to said data transfer switch, and configured to schedule simultaneous data transfers among a plurality of modules disposed within said multimedia processor, at least one of which is a cache memory, in accordance with corresponding channel allocations;

an interface unit coupled to said data streamer having a plurality of input/output (I/O) device driver units;

a multiplexer coupled to said interface unit for providing access between a selected number of said I/O device driver units to external I/O devices via output pins; and

a plurality of external I/O devices coupled to said multimedia processor.

REMARKS

Claims 19-37 are pending.

Claims 19-37 stand rejected.

Claims 19 and 28 have been amended.

No new matter has been added.

Claims 19-37 are hereby submitted for reconsideration.

In paragraphs 1 and 2 of the Final Office Action, the Examiner has rejected claim 37 under 35 U.S.C. §§ 112 and 132 for containing subject matter which was not disclosed in the original application as filed. Specifically, the Examiner states that the original specification does